

General Description

OIT6C consists in two silicon phototransistor's monolithic arrays. The phototransistors have a common collector on the back substrate, which is tied to two single pads and every emitter is accessible to specific pad. The optical pitch of the array is 0.60 mm, the LCC package electrical pitch is 1.27 mm. The active area of each element is 0.20 x 0.45 mm².

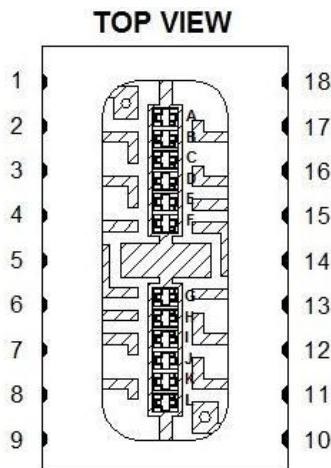
The advantages of this product are the high uniformity of the silicon sensors, due to the monolithic construction and to the extremely controlled microelectronic process, the high stability of the signal and the high optical responsivity, due to the antireflective coating deposited on the phototransistor's areas.

The packaging method is oriented to industrial harsh applications, which means high temperature range, high stability in time and very high uniformity of the silicon cells.



Applications

Optical encoders
 Incremental encoders
 12 bit absolute encoders
 Optical Receivers
 Controls/drives



Features

- High uniformity of silicon cells
- High transparency resin
- High gain
- Reference holes for precise mounting
- 0.6 mm optical pitch
- RoHS compliant

Pin Functions

No.	Name	Function
1	AE	Phototransistor A Emitter
2	CE	Phototransistor C Emitter
3	EE	Phototransistor E Emitter
4	N.C.	Not connected
5	N.C.	Not connected
6	GE	Phototransistor G Emitter
7	IE	Phototransistor I Emitter
8	KE	Phototransistor K Emitter
9	CC	Common collector
10	LE	Phototransistor L Emitter
11	JE	Phototransistor J Emitter
12	HE	Phototransistor H Emitter
13	N.C.	Not connected
14	N.C.	Not connected
15	FE	Phototransistor F Emitter
16	DE	Phototransistor D Emitter
17	BE	Phototransistor B Emitter
18	CC	Common collector

Ordering information

OIT6C 12-ch. phototransistor array 0.60mm optical pitch on plastic SMD package

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Min	Max	Unit
T_A	Operating Temperature Range	-40	85	°C
T_S	Storage Temperature	-40	85	°C
T_{Sol}	Lead Temperature (solder) 3s		230	°C
$V_{R(BR)}$	Breakdown Voltage Collector-Emitter @ $T_A=25^{\circ}C$ $I_B=100nA$ $I_C=1mA$	50		V
P_D	Power Dissipation @ $T_A=25^{\circ}C$		150	mW

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rated conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

$T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I_D	Dark Current	$V_R=10V$		5	100	nA
R_{λ}	Responsivity	$V_{CE}=5V$ $\lambda=880nm$	0.5			A/W
λ_p	Peak Responsivity	$V_{CE}=5V$		750		nm
$\Delta\lambda$	Spectral Bandwidth @ 50%	$V_{CE}=5V$	500		950	nm
I_{ec0}	Emitter-Collector Current	$V_{CE}=7.7V$		0.1	100	μA
I_{ce0}	Collector-Emitter Current	$V_{CE}=52V$		0.1	100	μA
H_{FE}	Gain	$V_{CC}=5V$ $I_C=2mA$	500	1100	1500	
$V_{CE(sat)}$	Saturation Voltage	$I_E=2mA$ $I_B=20\mu A$		80	200	mV
$I_{C(on)}$	On-state Collector Current	$V_{CE}=5V$ $E_E=1.0mW/cm^2$		1		mA

AC SWITCHING CHARACTERISTICS

$T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
t_R	Rise Time	$V_{CC}=5V$ $I_C=1mA$ $R_1=1k\Omega$		10		μs
t_F	Fall Time	$V_{CC}=5V$ $I_C=1mA$ $R_1=1k\Omega$		11		μs

MECHANICAL CHARACTERISTICS

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
A	Phototransistor Active Area			0.09		mm ²
L	Length of the Active Area			0.2		mm
W	Width of the Active Area			0.45		mm

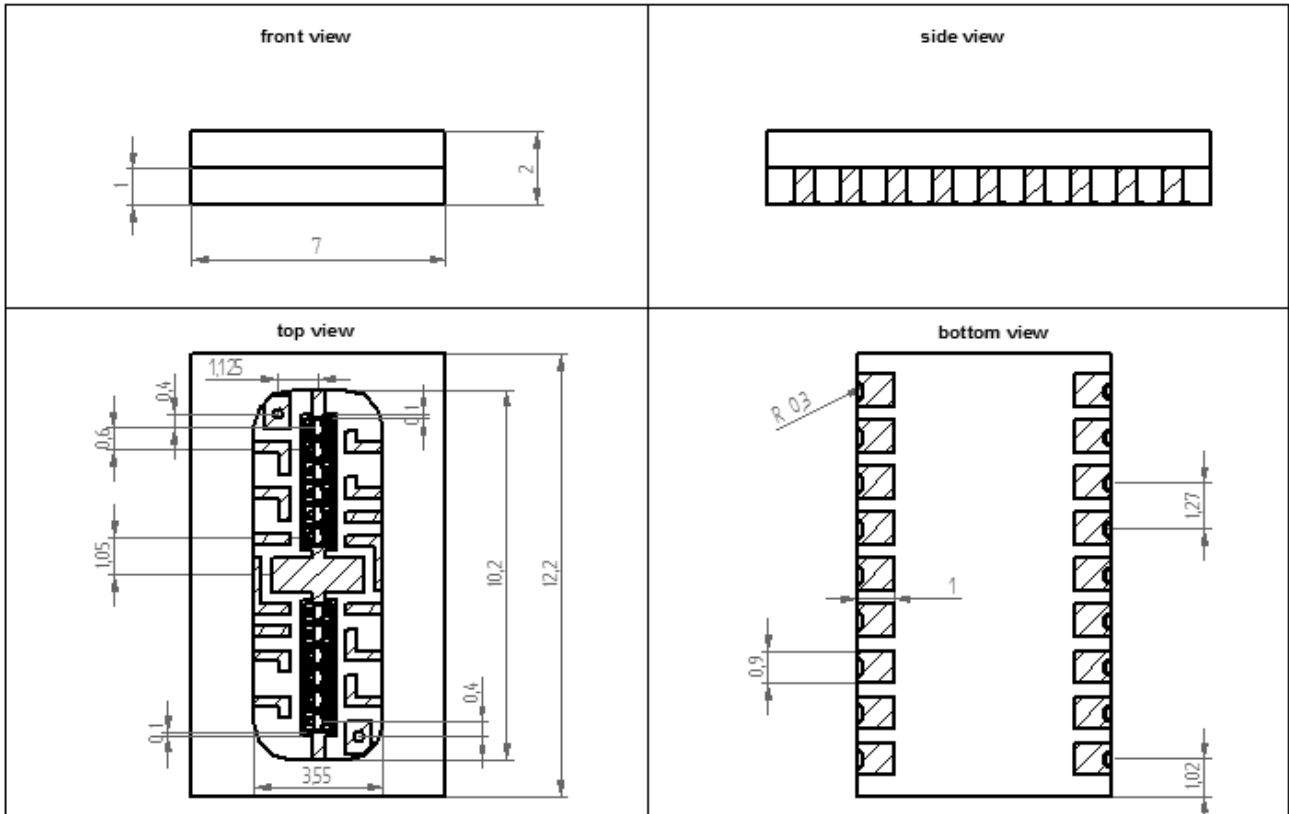
PACKAGE CHARACTERISTICS

Symbol	Parameter	Value	Unit
S_F	Pad Surface Finishing	GOLD	
S_L	Pad Shelf Life	6	months
MSL	Moisture Sensitive Level ‡	3	level

‡ According to Jedec standard J-STD-020D.1

MECHANICAL DIMENSIONS

Units=mm Mechanical tolerance= ± 0.2 mm Die positioning tolerance= ± 0.030 mm



TYPICAL PERFORMANCE CURVES

Figure 1 – Output voltage Vs Temperature

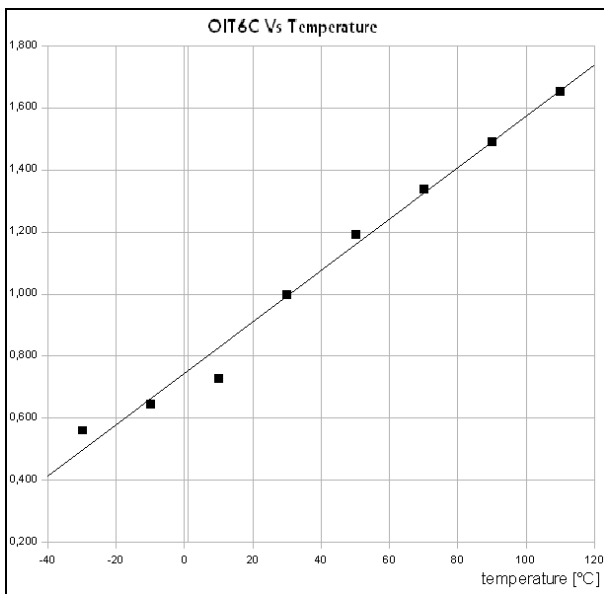


Figure 2 – Normalized spectral responsivity

